

Surface Mount Superfast Recovery Rectifiers
Reverse Voltage 50 to 600 Volts Forward Current 2.0 Amperes

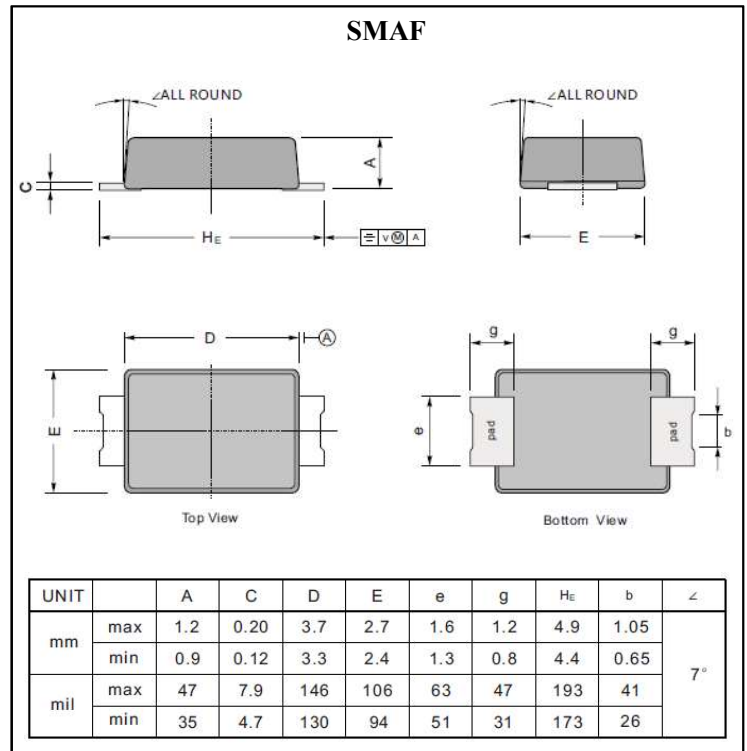
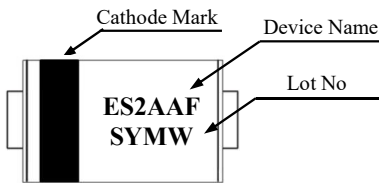
Features

- For surface mounted application
- Glass passivated junction chip
- Superfast reverse recovery time
- Low forward voltage drop
- High current capability
- High surge current capability

Mechanical Data

- Case : Molded plastic
- Terminals : Solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Weight : 0.027 gram

Marking



Maximum Ratings & Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified
Single phase half wave 60 Hz, resistive or inductive load
For capacitive load, derate current by 20%

Parameter	Symbol	ES 2AAF	ES 2BAF	ES 2CAF	ES 2DAF	ES 2FAF	ES 2GAF	ES 2JAF	Unit	Remark
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	50	100	150	200	300	400	600	V	
Maximum RMS Voltage	V _{RMS}	35	70	105	140	210	280	420	V	
Maximum DC Blocking Voltage	V _{DC}	50	100	150	200	300	400	600	V	
Maximum Average Forward Rectified Current	I _{F(AV)}	2.0							A	
Peak Forward Surge Current 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC Method)	I _{FSM}	50							A	
Maximum Instantaneous Forward Voltage @ 2.0A	V _F	1.00		1.30	1.70			V		
Maximum DC Reverse Current at Rated DC Blocking Voltage	I _R	5.0							uA	T _a =25°C
		200							uA	T _a =125°C
Maximum Reverse Recovery Time	t _{rr}	35.0							us	Note 1
Typical Junction Capacitance	C _J	45							pF	Note 2
Typical Thermal Resistance	R _{th(j-a)}	75							°C/W	Note 3
Operation Junction and Storage Temperature Range	T _J , T _{STG}	-55 to +150							°C	

Note 1. Reverse Recovery Time Test Conditions : I_F=0.5A, I_R=1.0A, I_{RR}=0.25A

Note 2. Measured at 1MHz and Applied Reverse Voltage of 4.0Volts D.C.

Note 3. Measured on P.C.Board with 0.5" × 0.5" (12.7mm×12.7mm) Copper Pad Areas.

Ratings and Characteristics Curves (Ta=25°C unless otherwise noted)

Fig.1 Forward Current Derating Curve

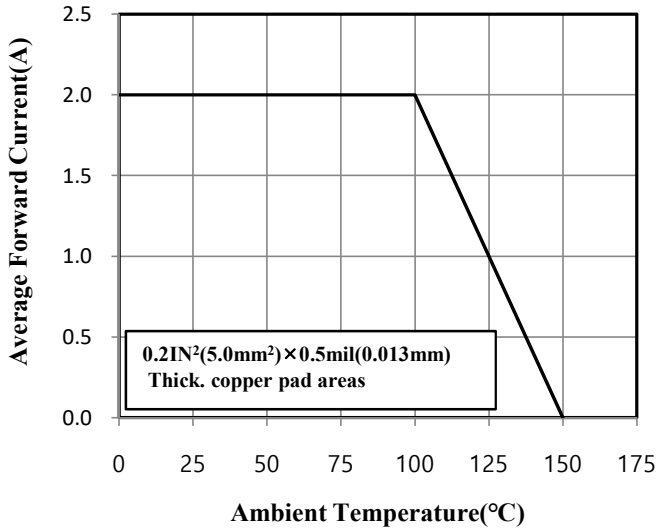


Fig.2 Maximum Non-Repetitive Peak Forward Surge Current

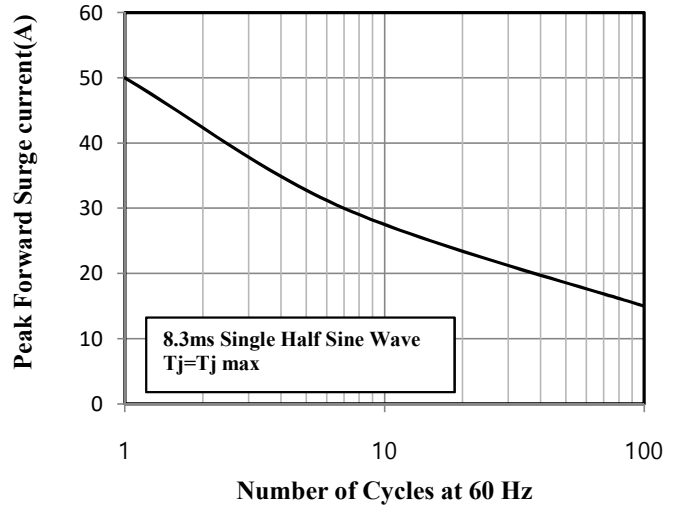


Fig.3 Typical Instantaneous Forward Characteristics

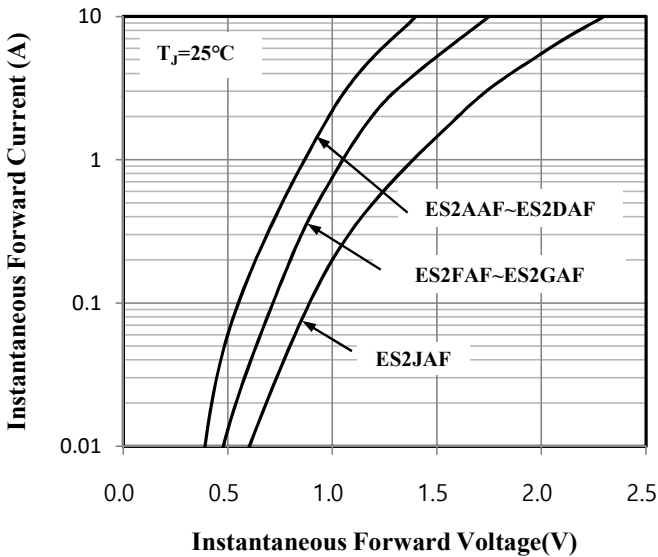


Fig.4 Typical Junction Capacitance

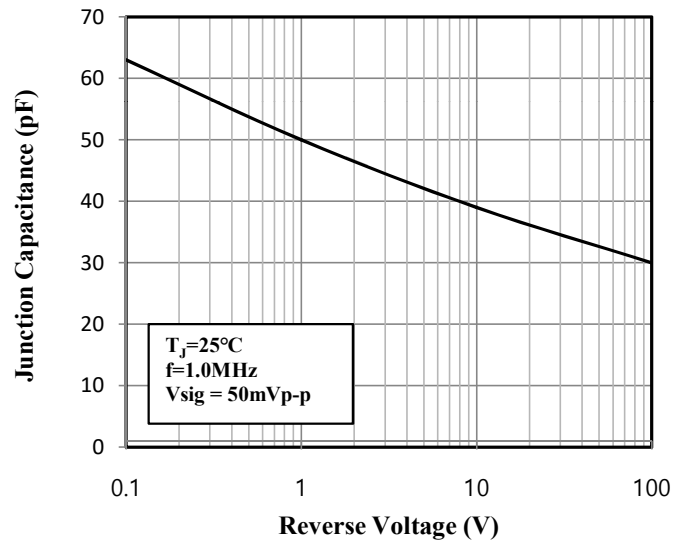


Fig.5 Typical Reverse Characteristics

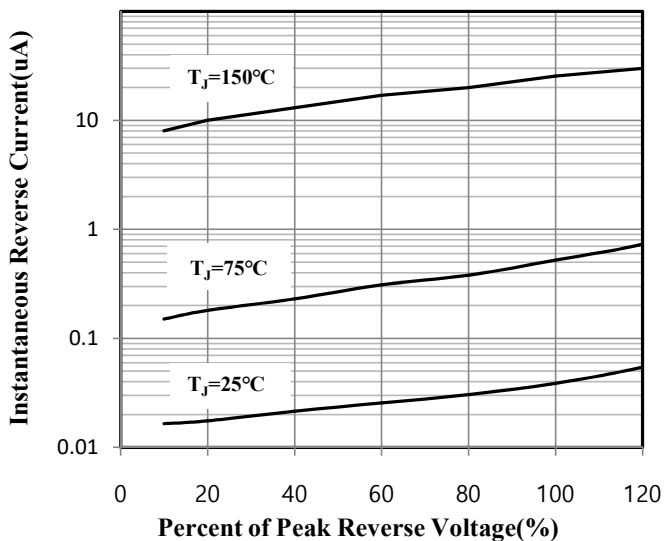


Fig. 6 Reverse Recovery Time Characteristic and Test Circuit Diagram

